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# **Dataflow programming languages:** Simulink

# Simulink

- Data-flow programming within MatLab, very engineering-oriented
- **PRO: Compile/<u>deploy</u> to many systems**
- Sapienza students licenses
- Android devices
- Apple iPhone/iPad
- Raspberry Pi
- Arduino
- Beagleboard







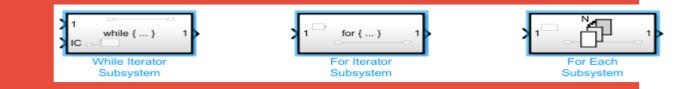
- Enter
- Xilink FPGA boards
- Lego Mindstorms EV3
- Parrot mini drones



# Features

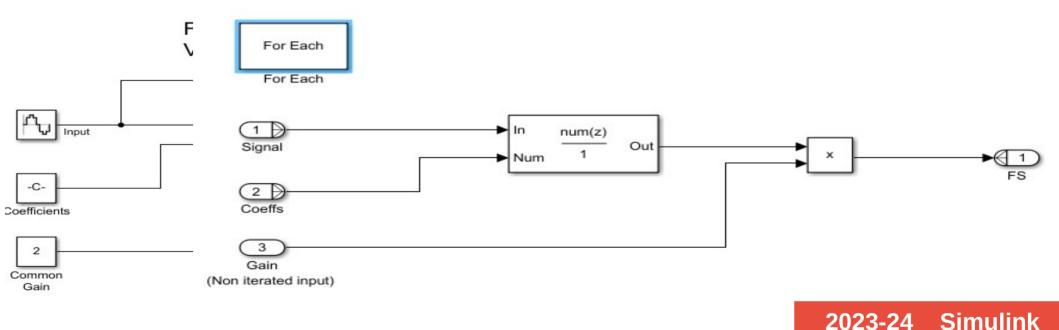
YES	(but not visually)
YES	(in Matlab or in Simulink)
NO?	
YES	(but in Matlab only)
YES	(for, foreach, while)
YES YES	(through Matlab)
YES	
YES	(subsystems)
YES	(explicit partitioning) 2023-24 Simulink
	YES NO? YES YES YES YES YES

# Subsystems: looping constructs



Subsystems are used for:

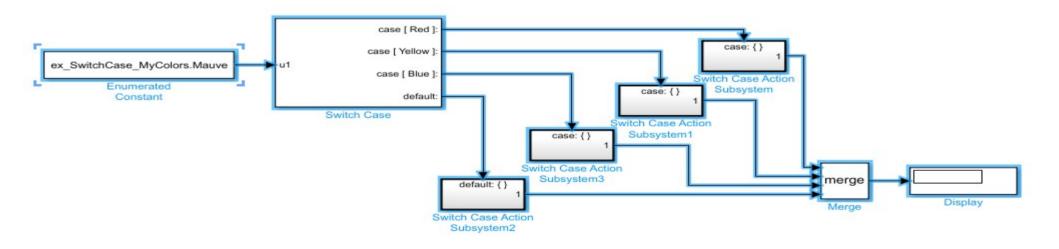
- Hierarchical model definition (modularization)
- Repeated execution (for/while/foreach)



#### **Conditionals: switch + merge**

**Conditional execution (if/case) is made by:** 

- if/case block with tested input and "enable" outputs
- a separate circuit/subsystem for each case (with "enable" port)
- a merge block collecting all alternate outputs



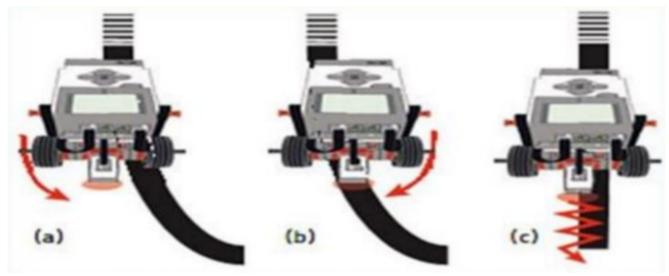
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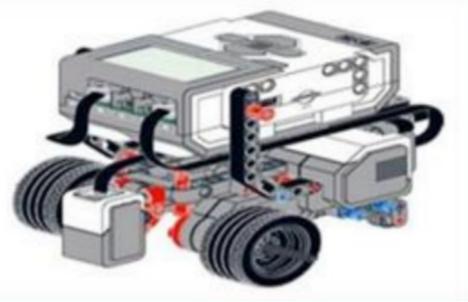
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#### Lego EV3 line follower

EV3 with light sensor facing down

# Follow the B/W border of the line



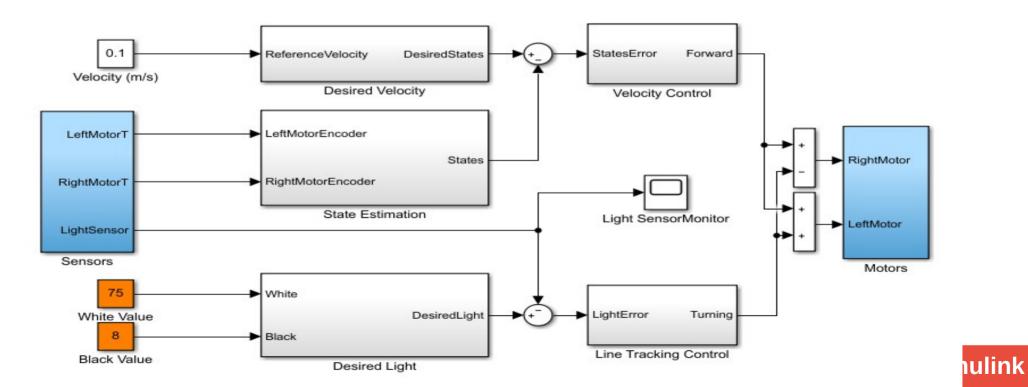


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#### Line follower: control system

#### Line Tracking

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#### Line follower details

SIMULINK ... loading



#### Lego Bike: keep a bicycle up by steering (@UNI-FI)



Sensors:

- gyroscope
- ultrasound distance
- front wheel angle
- rear wheel rotation

#### Actuators

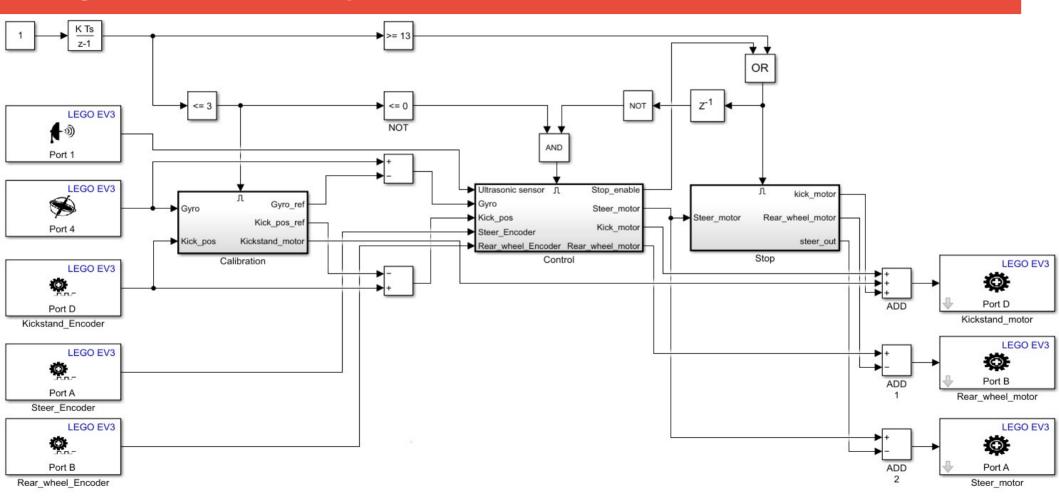
- front wheel steer angle

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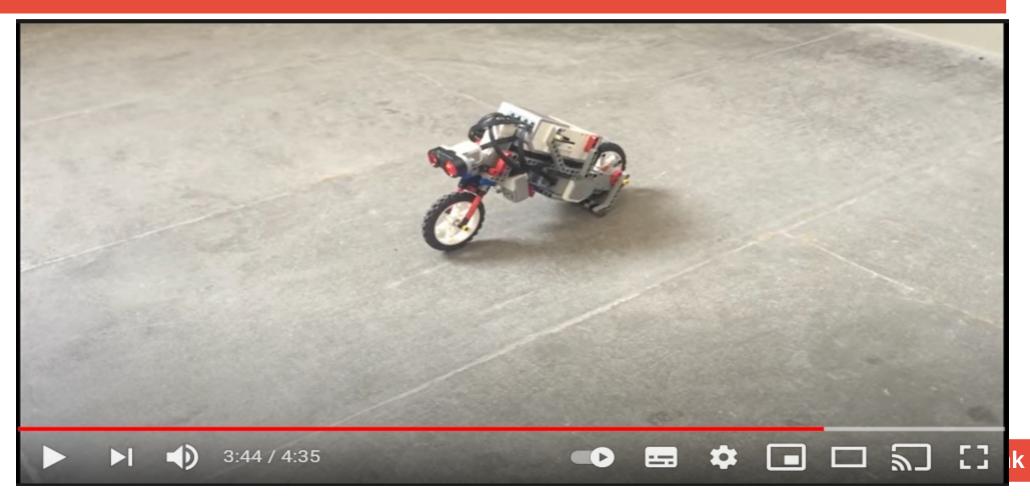
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- rear wheel speed
- rear stand up/down

#### Lego Bike: control system



#### Lego Bike in action



# Simulink for teaching Computational Thinking?

#### PRO

- algorithms as circuits
- enhance modularization with submodules
- good for data/signal analysis
- good for control systems
- a lot of packages and examples
- robotic simulation (ROS or Control toolbox)

#### CON

- algorithms as circuits (!)
- could be overwhelmingly complex
- diagrams less readable than LabVIEW
- exec. constraints not shown in diagrams

TLDR: good for electronic/technical schools

