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## **Dataflow programming languages:**

#### LabVIEW

## **Data-flow: interconnected functional units**

Functional units connected by wires

- wires represent data exchanges

- (i.e. variables)
- they could be typed (a different color/shape for each wire)
- many data can be aggregated in a single BUS (i.e. record)
- each functional unit has a default GUI for testing its I/O

Granularity

- functional units can be defined and reused
- circuits/networks can be packaged as new blocks

#### LabVIEW

Created by <u>National Instruments</u> to interact with digital data-acquisition and control systems

Modelled over the <u>circuit design and testing metaphor</u>

Each functional unit in the graphic language runs as soon all its input data are available

Multiple cores and threads are used to schedule the parallel execution of multiple active units

The programs are compiled into an intermediate "G" language (but can also be compiled to native code)

You normally (need to) add explanation boxes to document your ideas

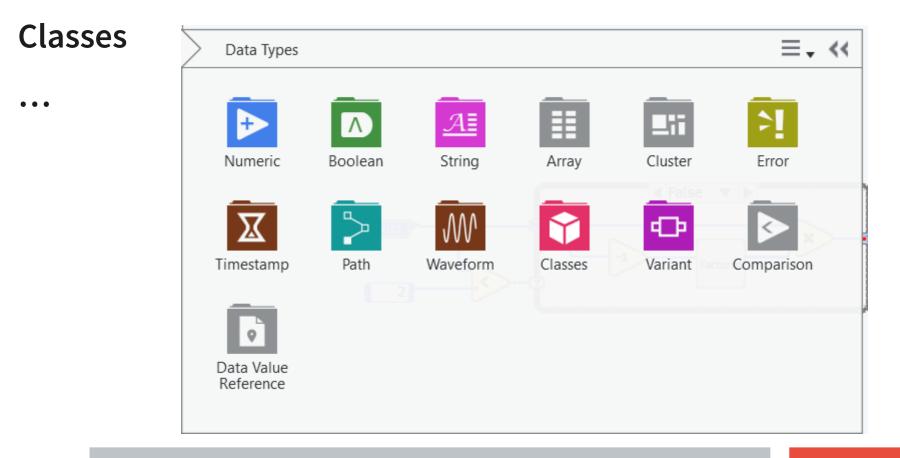
Free LabView Community edition available for personal usage

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#### **Data types**

Many numeric types (to interface with hardware)

#### Arrays and records (Clusters)

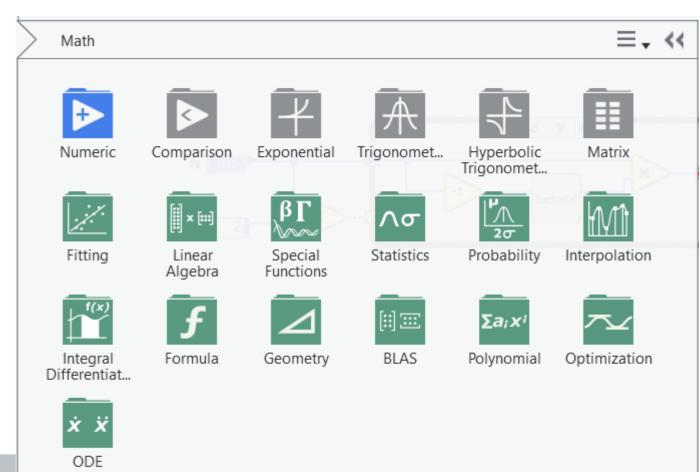


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## **Functional units**

#### Many numeric processing elements

- Multiple values can be bundled in buses
- Wires have types



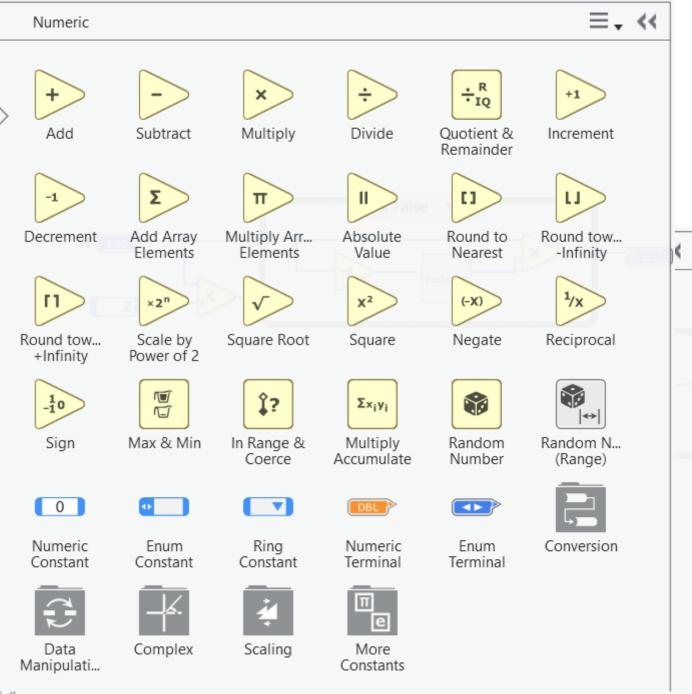
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## **Functional units**

Many numeric process

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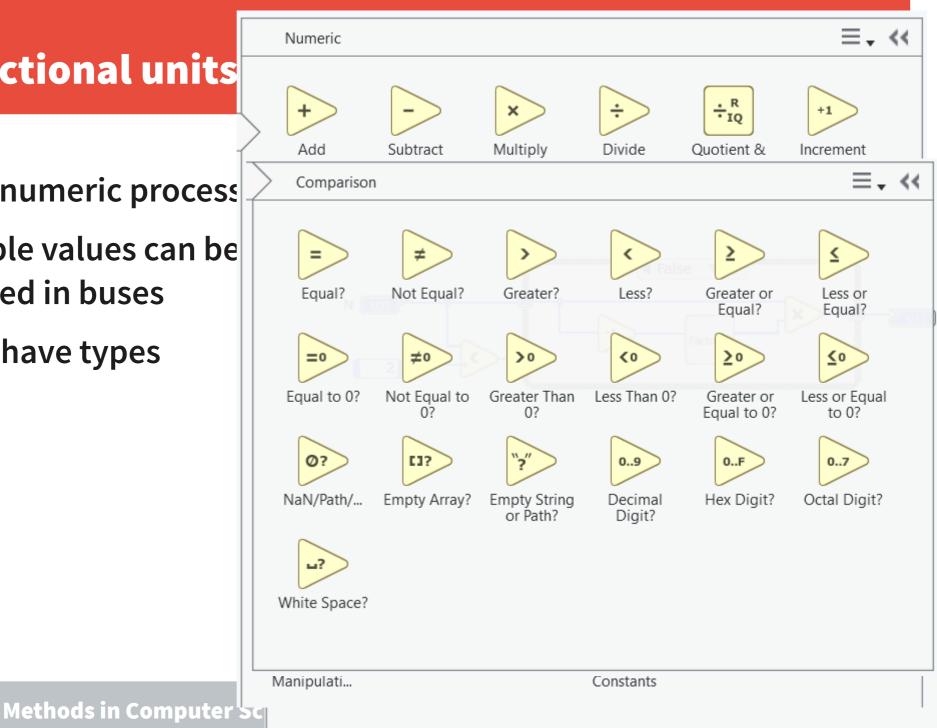


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# **Functional units**

Many numeric process

- Multiple values can be bundled in buses
- Wires have types



# Control structures and scope

	<ul> <li>"Option 1"</li> </ul>	<b>~</b> >
This diagram	executes if the Case Sele	ctorvalue is "Option 1"
nis diagram	executes in the Case Sele	ctor value is Option 1.

Control structures are represented as boxes

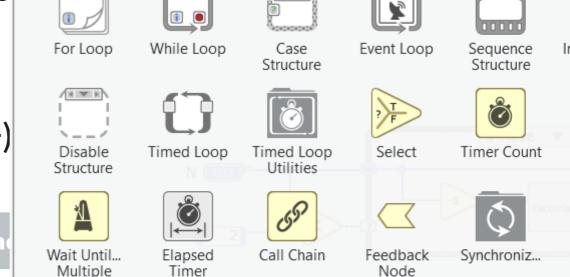
- on the border there is a <u>conditional/control input</u> connector
- the box is the equivalent of a parenthesis

Enum

- multiple cases (if-then-else, switch-case) become "pages"

Program Flow

- the box title contains the options of the case/condition
- all "pages" share the same external inputs / outputs
- control values (index) are present in all pages
- There are also boxes for <u>formulas</u> or <u>external code</u> (ASM/C/C++)



In Place Ele... Structure

 $\equiv -$ 

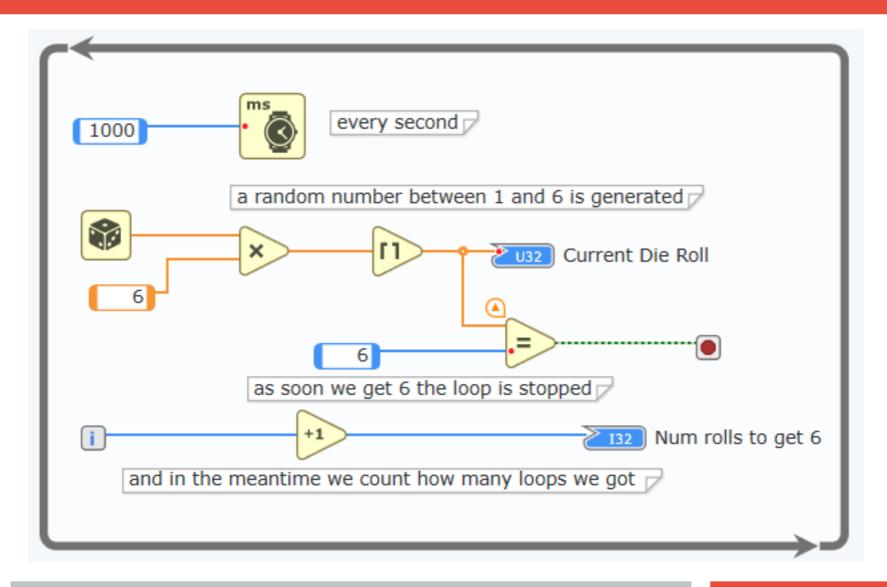
Wait

Execution Control

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#### While loop example



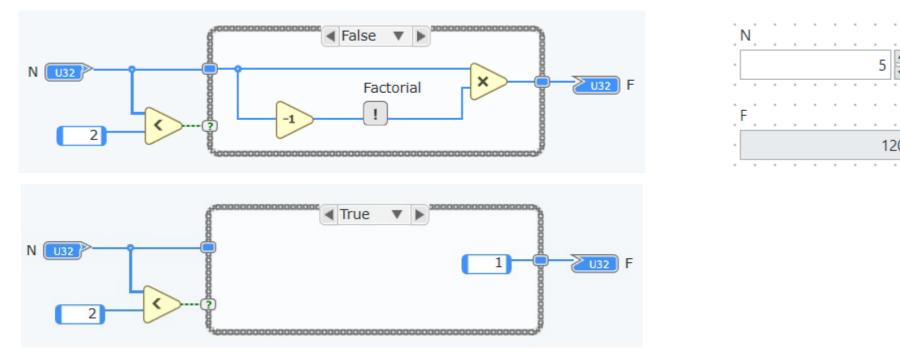


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#### **Recursion? YES**

Define a block as "reentrant" (i.e. allowing multiple parallel copies)

Then you can call it inside the same block or one of its sub-blocks



#### NOTE: you can also define "code" blocks with C

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## Concurrency

Inherently parallel

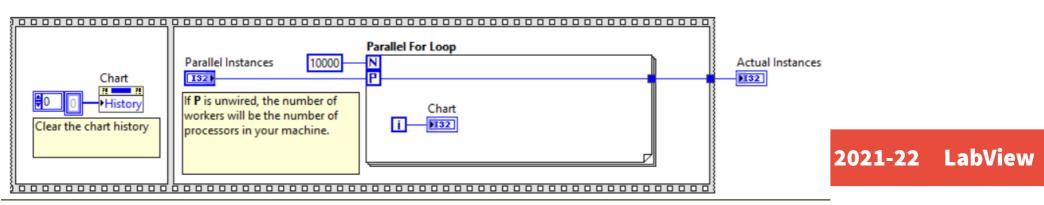
- linked units must run <u>sequentially</u> because of <u>data dependency</u>
- NON-linked units run in parallel (emulated)

Synchronization

- a block starts when <u>all input data is available</u>

Sequencing constraints

- data dependencies (links induce time order)
- you can add time dependencies without data exchange (or you could add data dependencies to do the same)



## LabView programming style

Data-flow visual design Visual construction of the data-flow diagram Visual test of the diagram all blocks have their GUI showing IN/OUT data <u>probes</u> can be added to show <u>internal</u> wires' values

Inherently parallel (you just forget about sequentiality constraints)

**Object-Oriented (classes)** 

Interaction with other systems:

- Function blocks for data math manipulation
- Code blocks for special algorithms
- Many libraries for Statistics, Signal analysis/manipulation, Math

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# Each functional unit has a GUI many widgets are available (active or read-only)

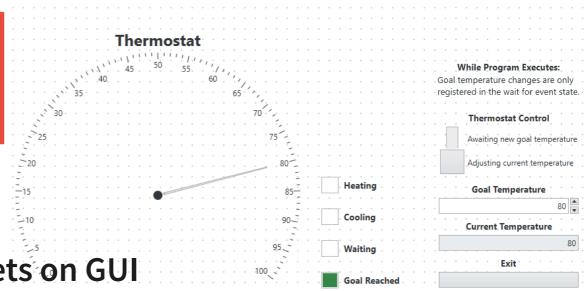
TEXT TEXT 2	
Hello world	
RÉSULT	
X ADD/MULT	
Υ	
0	
0,000000	
Loop iterations	
	· · · · ·
	· · · · · · · · · · · · · · · · · · ·
0 50 100 150 200 250 300 350 STOP AT	· · · · · ·
0       1	· · · · · ·
I       I	
I       I	· · · · · · · · · · · · · · · · · · ·
I       I	
I       I	

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## Debugging

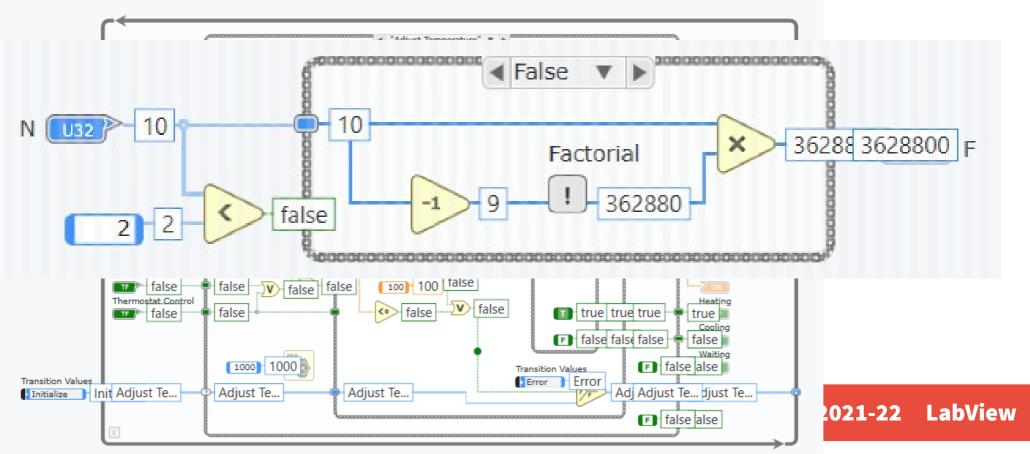
Visual tracing of data on wires GUI for blocks IN/OUT Probes on wires show as widgets on GUI Values are shown on wires

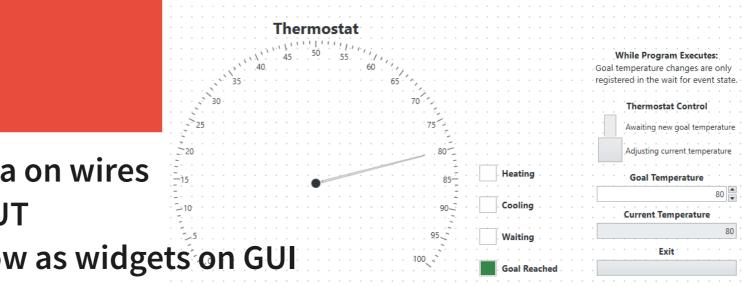
"Adjust Temperature" 🔻 Goal Temperature 80 80 80 80 Goal Reached 80 false Initial Temperature True 🔻 true > 52 52 52 52 50 50 52 Thermostat 52 53 53 false Exit Current Temperature 100 100 false false false Telse false Thermostat Control Heating V false T true true true false false false true Coolina F false fals€ false false Waiting F false alse 1000 1000 Transition Values Error Error Transition Values Adj Adjust Te... djust Te... Initialize Init Adjust Te... Adjust Te... Adjust Te... 021-22 LabView false alse



## Debugging

Visual tracing of data on wires GUI for blocks IN/OUT Probes on wires show as widgets on GUI Values are shown on wires







DEMO

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